

## AMENDMENTS TO THE SPECIFICATION

Page 6, paragraphs starting at line 13.

Referring to FIGURE 4, resistor **R405** in series combination with resistor **R406'** forms a 10:1 resistive voltage divider (where **R406'** refers to the combination of **R406** in shunt with the series combination of **R403** and **R404**). Capacitor **C406** in series with capacitor **C405'** forms a 10:1 capacitive divider in parallel with 10:1 resistive voltage divider (where **C405** refers to the combination of **C405** in shunt with the series combination of **C403** and **C404**). A second 10:1 resistive-capacitive voltage divider, formed by the series combination of resistor **R403** and resistor **R404** in parallel with series combination of capacitor **C403** and capacitor **C404**, is coupled to the center tap of voltage divider **R405**, **R406'**. This results in a cascade connection of two divide-by-10 arrangements for a total divider ratio of 100:1.

Page 8, paragraph starting at line 1.

Although the circuitry of FIGURE 5 also employs low frequency compensation, that compensation is applied at a later amplifier stage as a feed forward technique. The output signal **501a'** of a buffer amplifier **510** is applied to a high frequency signal path comprising resistor **R506** and to a low frequency signal path. The low frequency signal path comprises a variable gain amplifier **520** operating under control of a DAC **525**, an R-C low pass filter including resistor **R507** and capacitor **C507**, a buffer amplifier **540**, and a series resistor **R508**. Signals from the high frequency and low frequency signal paths are summed at the junction of resistors **R506** and **R508** and applied to a further variable gain amplifier **530** operating under control of a DAC **535**. In this design, the additional R-C time constant provided by R-C network **R507**, **C507** is matched to the time constant of the input R-C voltage divider network **R501**, **C501** and **R502**, **C502**. In this example **R507** has a value of 9k $\Omega$  and capacitor **C507** has a value of 100 pf.